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SCALPEL: A Projection Electron-Beam Approach to Sub-Optical Lithography

JA Liddle, LR Harriott, AE Novembre, WK Waskiewicz - Bell Laboratories, Lucent Technologies, New Jersey, December, 1999 - 140.114.18.41

... in terms of extensibility and **cost**-of-ownership ... of projection electron-beam **lithography**, high resolution ... employed, and cannot support **patterns** containing closed ...

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The Weakest Link: Semiconductor Production Equipment, Linkages, and the Limits to International ...

J Stowsky - University of California, Berkeley, 1986 - [brie.berkeley.edu](#)

... the effect of delaying equipment **delivery** or impairing ... lowered the **cost** and improved the ... of an independent, entrepreneurial **semiconductor** equipment industry in ...

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From Confrontation to Coopetition in the Globalized Semiconductor Industry.

AJW van de Gevel - [arno.uvt.nl](#)

... on the amount of memory, the **cost** per megabyte of ... Inherently the **semiconductor** industry is confronted with boom- bust ... in order to assure timely **delivery** of the ...

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Photomask Technology

MMMC Center - [spie.org](#)

... [5567-06] 11:35 am: Chromeless phase **lithography** reticle defect ... inspection study with high-speed mask inspection system, JJ Jea Young, Hynix **Semiconductor** Inc. ...

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... characteristics at the lowest possible **cost**. ... software which enables **semiconductor** manufacturers to speed development of advanced **lithography** processes required ...

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Planning and Scheduling in Supply Chains: An Overview of Issues in Practice

S Kreipl, M Pinedo - Production and Operations Management, 2004 - [extenza-eps.com](#)

... oxidation, deposition, and metalli- zation, **lithography**, etching, ion ... In **semiconductor** and Printed Circuit Board manufactur ... min- imize the total **cost** over all ...

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... each of the key process modules: **lithography**, etch ... dramatically reducing the time and **cost** of creating ... The **semiconductor** fabrication process begins with a bare ...

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[PS] Incremental algorithms for general purpose layout system

MH Cynn - 1994 - [historical.ncstrl.org](#)

... survive in the competitive IC markets, not only the **cost**, but also fast **delivery** without ... Thus, reducing the-time-to-market for a **semiconductor** product is ...

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JA Liddle, LR Harriott, AE Novembre, WK Waskiewicz - bell-labs.com

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K Seshan - 2001 - print.google.com

... The **lithography** machines for creating the desired precise and fine-scaled **patterns** now **cost** several million ... that grew up around **semiconductor** inte- grated ...[Web Search](#)

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S Kreipl, M Pinedo - Production and Operations Management, 2004 - extenza-eps.com

... the impact of the **Internet** on decision ... deposition, and metalli- zation, **lithography**,

etching, ion ... In **semiconductor** and Printed Circuit Board manufactur- ing ...

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... of the key process modules: **lithography**, etch, ... Consistent with the focus on

Internet-enabled software ... The **semiconductor** fabrication process begins with a bare ...

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Advanced Technology and the Future of US Manufacturing

P Shapira, J Youtie, A Urmanbetova - cherry.iac.gatech.edu

... exceeded expected adoption rates (eg, the **Internet** in the ... s transistors, (2) controlled

drug **delivery**, and (3 ... that are less invasive, more **cost**-effective, and ...

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Prime Faraday Technology Watch

PF Partnership - primetechnologywatch.org.uk

... MST is that MEMS tends to use **semiconductor** processes to ... early external blood pressure

sensors that **cost** over \$600 ... caused by the rapid rise of the **Internet**. ...

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An Introduction to MEMS (Micro-electromechanical Systems)

PFT Watch - 2001 - biomems.net

... MST is that MEMS tends to use **semiconductor** processes to ... early external blood pressure

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... IBM to significantly expand its **semiconductor** offerings. ... was Director of Intranet and **Internet** data networking ... in system implementation from low **cost**, bus-based ...

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Limitations and challenges of computer-aided design technology for CMOS VLSI

R Bryant, KT Cheng, A Kahng, K Keutzer, W Maly, R ... - Proceedings of the IEEE, 2001 - ieeexplore.ieee.org

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China's gains in the **semiconductor** market could cause pains for other ...
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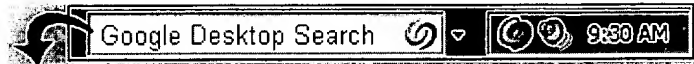
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